

AMENDMENTS TO THE SPECIFICATION

Please amend the paragraph beginning on page 1, line 5 of the specification as follows:

This application is a continuation of 10/318,586, filed December 13, 2002, now U.S. Patent No. 6,748,041 B1, which is a continuation of U.S. Application No. 09/540,243, now U.S. Patent No. 6,526,113, entitled "GM Cell Based Control Loops," filed March 31, 2000, by German Gutierrez et al., which claims the benefit of the following U.S. provisional patent applications:

Please amend the paragraph beginning on page 11, line 25 of the specification as follows:

The drains of transistors 610a and 610b couple to respective current sources 614a and 614b of current load circuit 614 and form the current output signals IOUTP and IOUTN, respectively. Each of current sources 614a and 614b is implemented with a transistor having its gate coupled to CMFB circuit 616, which provides a bias control voltage that sets the currents of current sources 614a and 614b such that the average currents on IOUTN and IOUTP are at the proper values. CMFB circuit 616 receives the output signals IOUTP and IOUTN, determines the average current, and sets the bias control voltage accordingly. An exemplary circuit implementation for CMFB circuit 616 ~~[[us]]~~ is provided in Fig. 6B.

Please amend the paragraph beginning on page 17, line 19 of the specification as follows:

Fig. 10 is a block diagram of an embodiment of a phase locked loop 1000 that includes ~~[[to]]~~ a section for locking a VCO to a reference clock REF_CLK and another section for locking the VCO to the input data stream. All or a portion of phase locked loop 1000 can be implemented within transmit phase locked loop 932 in Fig. 9A and receive phased locked loop 952 in Fig. 9B. For example, transmit phase locked loop 932 can be implemented with just the section that locks the VCO to the reference clock, and receive phase locked loop 952 can be implemented with both sections to allow the VCO to be locked to the reference clock and the input data stream.